DRAWINGS

Please amend the drawings as follows.

Replace FIG. 1 and FIG. 2 with the attached redlined sheet FIG. 1 and FIG. 2A (1 sheets).

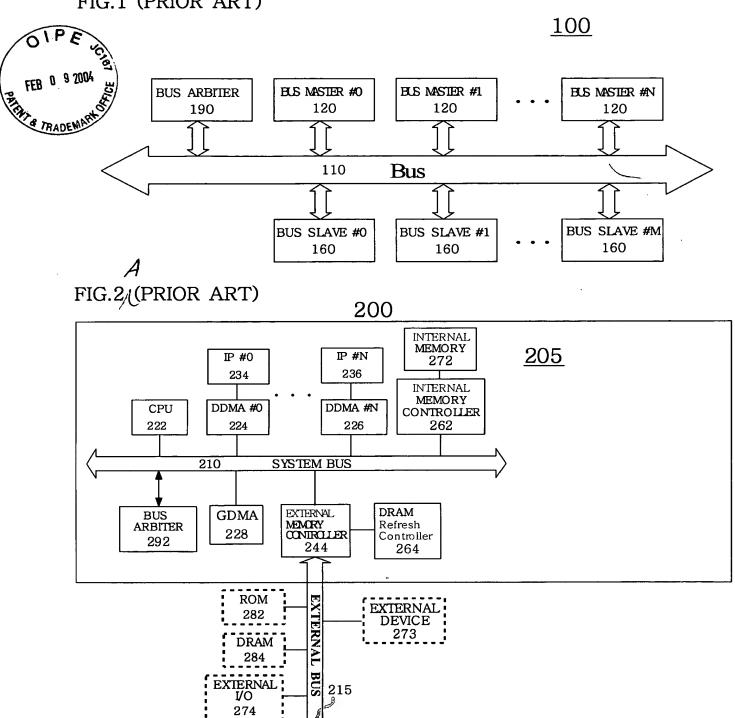
Replace FIG. 4 with the attached redlined drawing FIG. 4 (1 sheets).

Replace FIG. 5 with the attached redlined drawing FIG. 5 (1 sheets).

Replace FIG. 6 with the attached redlined drawing FIG. 6 (1 sheets).

Annotated Sheet Showing Changes





Young-Sik Kim, et al.

SYSTEM ON A CHIP HAVING SYSTEM BUS, EXTERNAL BUS, AND BUS ARBITER THE PROGRAMMABLE PRIORITIES FOR BOTH
BUSES, SOFTWARE, AND METHOD FOR ASSIGNING PROGRAMMABLE PRIORITIES

Docket No. 9898-172/Application No. 09/773,874

Annotated Sheet Showing Changes



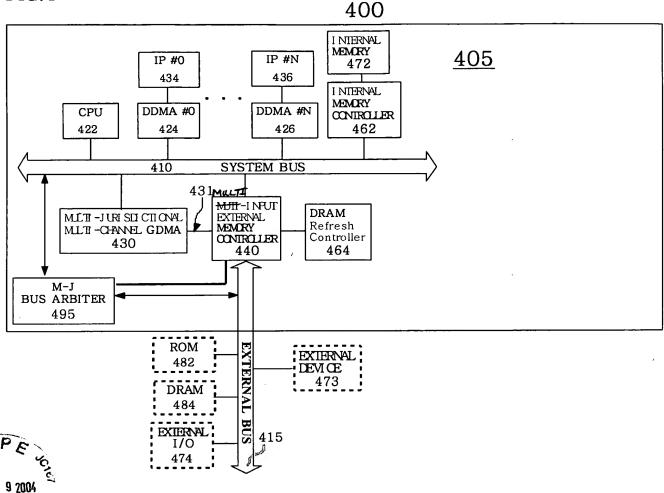
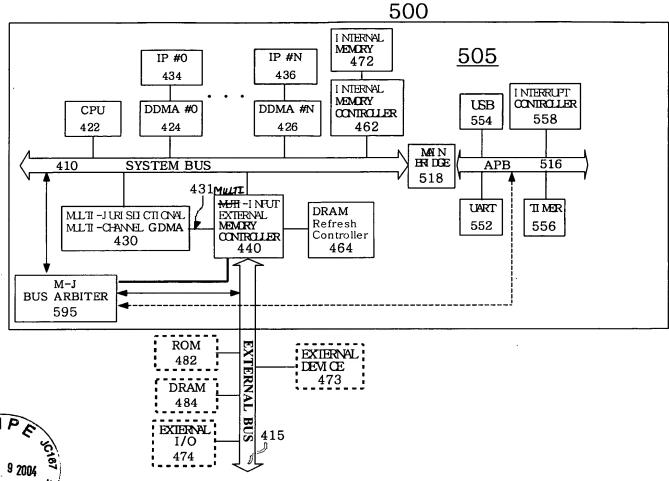


FIG.5 Annotated Sheet Showing Changes



Annotated Sheet Showing Changes FIG.6 600 I NIERNAL MEMORY <u>605</u> IP #N IP #0 472 436 434 I NIERNAL I NIERRUPT CONIRCLLER **MEMORY** USB DDMA #0 DDMA #N CPU CONTROLLER 558 554 424 426 462 422 MAIN AHB SWITCH BR DŒ APB 610-R AHBR 516 610-L AHBL 518 431 MILLE MJT - I NPUT EXTERNAL MEMORY TI MER **UART** DRAM MLTI -JURI SDI CTI ONAL Refresh 552 556 MLTI-CHANNEL GDMA Controller CONTROLLER 430 440 464 M-JBUS ARBITER 695 ROM EXTERNAL EXTERNAL 482 DEM Œ 473 DRAM 484 EXTERNAL BUS 415

I/O 474